

## APRIL NEWSLETTER "Shelter in Place" Edition

### Productivity in the COVID Era

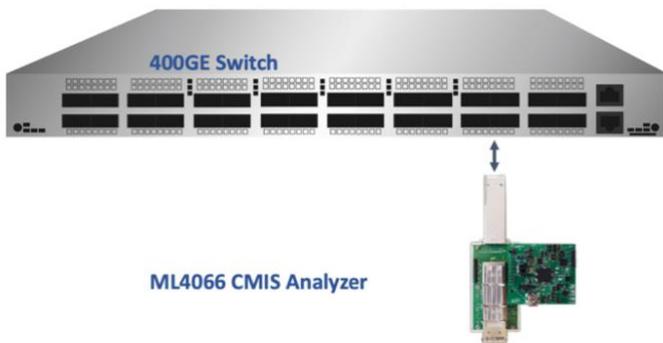
We sincerely hope that everyone is doing well and safely sheltering-in-place with their families and loved ones. MultiLane continues to operate thanks to our work-from-home team; we are all striving to make sure we continue to support our community with their test and measurement needs.

This newsletter provides an overview of exciting new test capabilities that equip our customers to overcome a wide variety of testing challenges.

### Smart Loopbacks

As the global leader in Data Center Interconnect port validation, MultiLane is reintroducing the high-speed loopback with a brand-new feature set. In addition to superior signal integrity, **MultiLane Smart Loopbacks** feature programmable power consumption, temperature/voltage/current sensors and CMIS verification. Users can leverage convenient tools such as LCD displays, LED indicators and pin headers to monitor these diagnostics as they see fit.

### CMIS Testing



A sometimes overlooked yet important element of data center interconnects is the low speed I2C CMIS interface. MultiLane has developed solutions to program and test the CMIS interface of transceivers for key MSA publications. A user can determine if a DUT is truly compliant with CMIS 3.0, 4.0, 4.1, be it a host or an optical module. Features like State Machine Analysis and I2C Signal Monitoring effectively identify any gaps between the CMIS interpretation between modules and hosts.

To find out more about these exciting new test capabilities please contact us at [sales@multilaneinc.com](mailto:sales@multilaneinc.com) to schedule a consultation with one of our experts.

**Please stay safe, stay healthy, and stay at home - The MultiLane Team**

### FEC White Paper

MultiLane has compiled an educational report with a primary focus on how Forward Error Correction schemes help enable 100G-per-lambda transceiver designs. This paper aims to be an educational resource for those who want to understand the critical role FEC plays in closing error-prone optical links, along with an overview of the **Real FEC** measurement capabilities possible with MultiLane BERTs. Link to document [here](#).

### Switch Host Port Testing

MultiLane is proud to unveil a unique solution suite aimed at stress testing host environments. Test the limits of your DUT, be it a SERDES chip or a full 400GE host platform, with noise injection in the new **ML4039EN** BERT Platform. This consolidated tool set boasts the capability to execute Transmit Compliance, Stressed Receiver, FEC Tolerance and Jitter Tolerance Testing through various operational modes.

### 400G Compliance and Interop Testing



MultiLane will be opening the doors to a brand new 400G Compliance and Interoperability lab by the end of Q2. This environment will feature industry-leading 400GE switches, MultiLane test gear and thermal test chambers. Bring your optics to our Lab in Fremont, California to acquire a complete picture of how they will fare in a real production environment. Assess module compliance in various switch ports, or test interoperability across different module and switch vendors, all at the ambient temperature of your choice thanks to our walk-in thermal chambers.