

CFP2 Retimed Loopback Module

ML4043

CFP2 Loopback module
(10x10G) Interconnects



Key Features

- CFP2 MSA Form Factor
- Retimed Loop back mode at 100GbE 10x10.3125G rates
- Wide range CRU
- MDIO MSA compliant slave
- Custom memory maps
- Input & DFE adaptation to optimize eye opening
- Loss of signal detection
- MSA compliant functionality
- LED indicator
- Temperature Monitor
- Insertion counter

Ordering information

ML4043

Superior Integrity and Performance

Summary

CFP2 Retimed Loopback Module **ML4043**, is designed to provide an efficient and easy method of characterizing and testing 10x10G CFP2 ports.

The **ML4043** comes packaged in a MSA compliant shell housing offering excellent heat dissipation.

Applications

- 10x10G Electrical module testing and characterization
- High-speed data interface between an ASIC (SerDes) and the CFP2 module

Worldwide

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About MultiLane SAL

MultiLane SAL is leading developer of high speed instruments and interconnects test modules for 10, 40, and 100Gbps of SerDes and high speed IO for the semiconductor and cloud computing infrastructure. Products includes BERTs, Scopes, and MSA Compliant development tools MCBs, HCBs and loopback modules for CFP, CFP2, CFP2-ACO, QSFP, QSFP28, SFP and SFP28 modules. MultiLane's products are used to test semiconductors, AOC, electro optical modules and blades. MultiLane operates out of Houmal Technology Park in Lebanon, and has been offering leading edge technology and products to Tier-1 equipment suppliers globally.

Brochure History

This section describes the changes that were implemented in this document. The changes are listed by revision, starting with the most current publication.

Revision 0.1: May 21th 2015